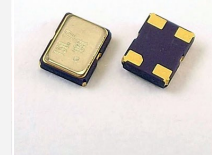


TCXO/VCTCXO ULTRA MINIATURE SMD (3.2 x 2.5 x 1.0mm)

FEATURES:
DIGITALLY COMPENSATED
LOW VOLTAGE / TIGHT STABILITY
LOW COST FOR A TIGHT STABILITY TCXO

APPLICATIONS:

- Cellular Phone, CDMA
- GSM, GPS
- SONET, Microprocessor

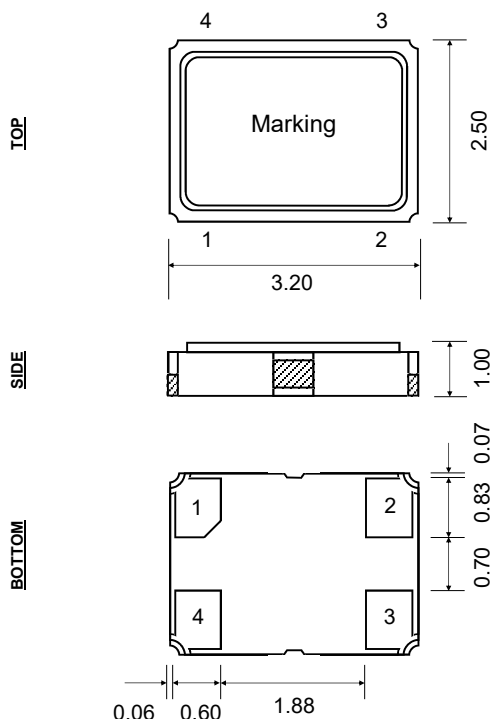


■ SPECIFICATION

PARAMETER		MIN.	TYP.	MAX.	UNIT	NOTE
FREQUENCY RANGE		8		40	MHz	
STANDARD FREQUENCIES		10.000MHz, 13.000MHz, 19.200MHz, 19.680MHz, 25.000MHz, 26.000MHz				
FREQUENCY TOLERANCE				±1.0	ppm	@ V _c = 1/2V _{DD} DC @ 25°C ± 2°C
FREQUENCY STABILITY	vs. TEMPERATURE RANGE			±1.0	ppm	See P/N guide for other options
	vs. LOAD ± 5%			±0.2	ppm	
	vs. SUPPLY VOLTAGE ± 5%			±0.2	ppm	
	vs. AGING @ 25°C			±1.0	ppm/year	±5.0ppm max for 10 years
OPERATING TEMPERATURE RANGE		-40		+85	°C	See P/N guide for other options
STORAGE TEMPERATURE RANGE		-40		+85	°C	
SUPPLY VOLTAGE ±5%		2.5		3.6	V	See P/N guide for other options
SUPPLY CURRENT	CMOS			6.0	mA	
	CSW			2.0	mA	
OUTPUT	LOGIC FAMILY	CMOS / Clipped Sine wave (DC-coupling)				
	LOAD	CMOS	15pF			
		CSW	10kΩ/10pF			
	LEVEL	CMOS (V _{OH})	0.8 x V _{DD}		V	
		CMOS (V _{OL})		0.2 x V _{DD}	V	
		CSW	0.8		V _{P-P}	
	SYMMETRY (DUTY CYCLE)		40	60	%	
	RISE AND FALL TIME (Tr/Tf)			6.0	nS	For CMOS
	START-UP TIME			3.0	mS	
FREQUENCY DEVIATION		±3.0		±15.0	ppm	See P/N guide for other options
CONTROL VOLTAGE		0		V _{DD}	V	Center = 1/2 V _{DD}
SLOPE		Positive				
PHASE NOISE @ 12.8MHZ		1kHz		-130	dBc/Hz	

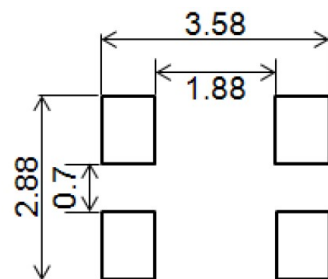
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■ PACKAGE DIMENSIONS



Pin Configuration	
1	GND or VC
2	GND
3	Output
4	V _{DD}

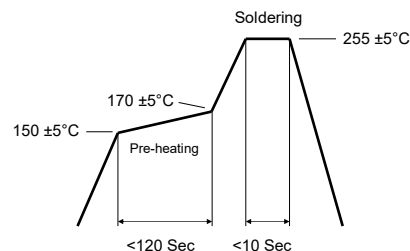
■ SOLDER PATTERN



Note : Please connect a bypass capacitor
Closely to V_{cc} Pad.(10μF recommended)

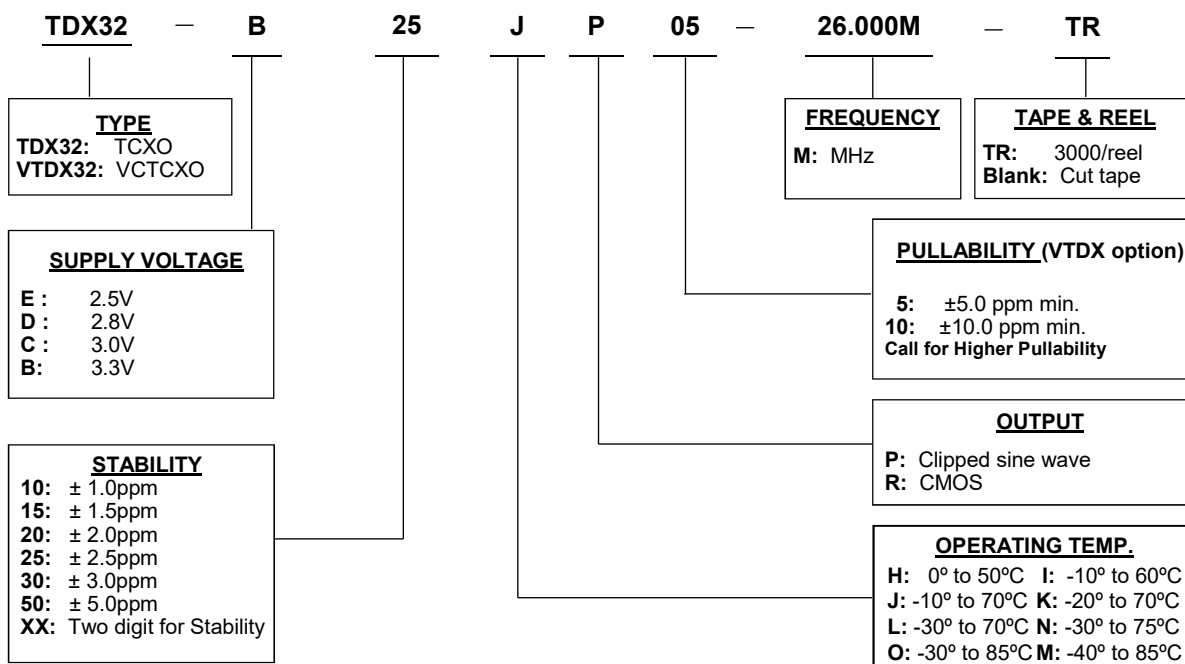
Unit : mm

■ REFLOW PROFILE



Note: Consult factory for detail

■ PART NUMBERING GUIDE



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